

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	130510	(packag\$3 chip on\$chip fpga pld) near6 (processor\$1 micro\$processor\$1 control\$4 micro\$control\$4)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/12/07 18:33
S2	643474	(power\$3 voltage\$1) near6 (control\$4 manag\$3 management sequenc\$3)	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/12/07 18:34
S3	14306	S1 same S2	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/12/07 18:34
S4	41	S3 and 713/330.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/12/07 18:53
S5	12722	wait\$3 near6 (predetermined near3 (time period))	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/12/07 18:53
S6	209	S3 and S5	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/12/07 18:53
S7	4	S6 and 713/330.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/12/11 15:05
S8	428860	(power voltage) near6 (manag\$3 management sequenc\$3 processor\$1 micro\$processor\$1 control\$4 micro\$control\$4)	USPAT	OR	OFF	2006/12/11 15:19
S9	7795	(packag\$3 chip on\$chip fpga pld) near6 S8	USPAT	OR	OFF	2006/12/11 15:19
S10	46	S9 and 713/330.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/12/11 15:19
S11	336719	(power voltage) near3 (manag\$3 management sequenc\$3 processor\$1 micro\$processor\$1 control\$4 micro\$control\$4)	USPAT	OR	OFF	2006/12/11 16:46
S12	5303	(packag\$3 chip on\$chip fpga pld) near6 S11	USPAT	OR	OFF	2006/12/11 15:20

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S13	46	S10 and 713/330.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/12/11 15:20
S14	1112	(packag\$3) near6 S11	USPAT	OR	OFF	2006/12/11 15:20
S15	9	S14 and 713/330.ccls.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/12/11 16:47
S16	5	"732216".ap.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2006/12/11 15:26
S17	10	("5560022"   "5721933"   "5742833"   "5787294"   "5872983"   "6601178"). PN. OR ("6735706").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/12/11 16:00
S18	4	("5603043"   "6438738"   "6490714"   "6735706").PN. OR ("6901572"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/12/11 16:13
S19	333245	(power voltage) near3 (sequenc\$3 processor\$1 micro\$processor\$1 control\$4 micro\$control\$4)	USPAT	OR	OFF	2006/12/11 16:46
S20	7668	wait\$3 near6 (predetermined near3 (time period))	USPAT	OR	OFF	2006/12/11 16:47
S21	280	S19 same S20	USPAT	OR	OFF	2006/12/11 16:47
S22	5	S21 and 713/330.ccls.	USPAT	OR	OFF	2006/12/11 16:48
S23	337149	(power voltage) near3 (manag\$3 management sequenc\$3 processor\$1 micro\$processor\$1 control\$4 micro\$control\$4)	USPAT	OR	OFF	2006/12/12 11:00
S24	1114	(packag\$3) near6 S23	USPAT	OR	OFF	2006/12/12 11:01
S25	9	S24 and 713/330.ccls.	USPAT	OR	OFF	2006/12/12 11:01
S26	333670	(power voltage) near3 (sequenc\$3 processor\$1 micro\$processor\$1 control\$4 micro\$control\$4)	USPAT	OR	OFF	2006/12/12 11:00
S27	4997	(packag\$3 chip on\$chip fpga pld) near6 S26	USPAT	OR	OFF	2006/12/12 11:01
S28	29	S27 and 713/330.ccls.	USPAT	OR	OFF	2006/12/12 11:29
S29	22	S28 not S25	USPAT	OR	OFF	2006/12/12 11:18
S30	14	S29 and storage\$1	USPAT	OR	OFF	2006/12/12 11:28
S31	10	S29 and (nic (network adj1 interfac\$) modem bridge (network adj1 hub\$1))	USPAT	OR	OFF	2006/12/12 11:29
S32	146	S27 same (nic (network adj1 interfac\$) modem bridge (network adj1 hub\$1))	USPAT	OR	OFF	2006/12/12 11:51

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S33	4	S32 and 713/330.ccls.	USPAT	OR	OFF	2006/12/12 11:52
S34	122	S27 same (a/d adc (analog\$digital adj1 convert\$4))	USPAT	OR	OFF	2006/12/12 12:05
S35	47	S27 near10 (a/d adc (analog\$digital adj1 convert\$4))	USPAT	OR	OFF	2006/12/12 11:53
S36	5	S35 and ((voltage power) near3 sequenc\$3)	USPAT	OR	OFF	2006/12/12 11:55
S37	40	713/330.ccls. and (a/d adc (analog\$digital adj1 convert\$4))	USPAT	OR	OFF	2006/12/12 12:05
S38	1	"6850048".pn.	USPAT	OR	OFF	2006/12/12 15:58